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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

EDWARD C. STEWART

Serial No.: 10/085,829

Filed: FEBRUARY 28, 2002

For: ANALYZING ERROR SIGNALS
BASED ON FAULT
DETECTION

Group Art Unit: 2125

Examiner: Alexander J. Rosowski

Conf. No.: 1732

Atty. Dkt.: 2000.087300/TT4580
CUSTOMER NO.: 23720

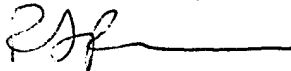
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JUN 24 2004

RESPONSE TO OFFICE ACTION MAILED MARCH 16, 2004 Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8	
DATE OF DEPOSIT:	June 16, 2004
I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
 _____ Signature	

This paper is submitted in response to the Office Action mailed March 16, 2004, for which the shortened, three-month statutory response date is June 16, 2004. Since this response is filed on June 16, 2004, this action is timely filed.

If an extension of time is required to enable this paper to be timely filed and there is no separate Petition for Extension of Time filed herewith, this paper is to be construed as also constituting a Petition for Extension of Time Under 37 CFR § 1.136(a) for a period of time sufficient to enable this document to be timely filed.

No fee is believed due as a result of the response contained herein. However, **should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason, the Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT4580/2000.087300.** In the event the monies in that account are insufficient, the Commissioner is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.087300.